

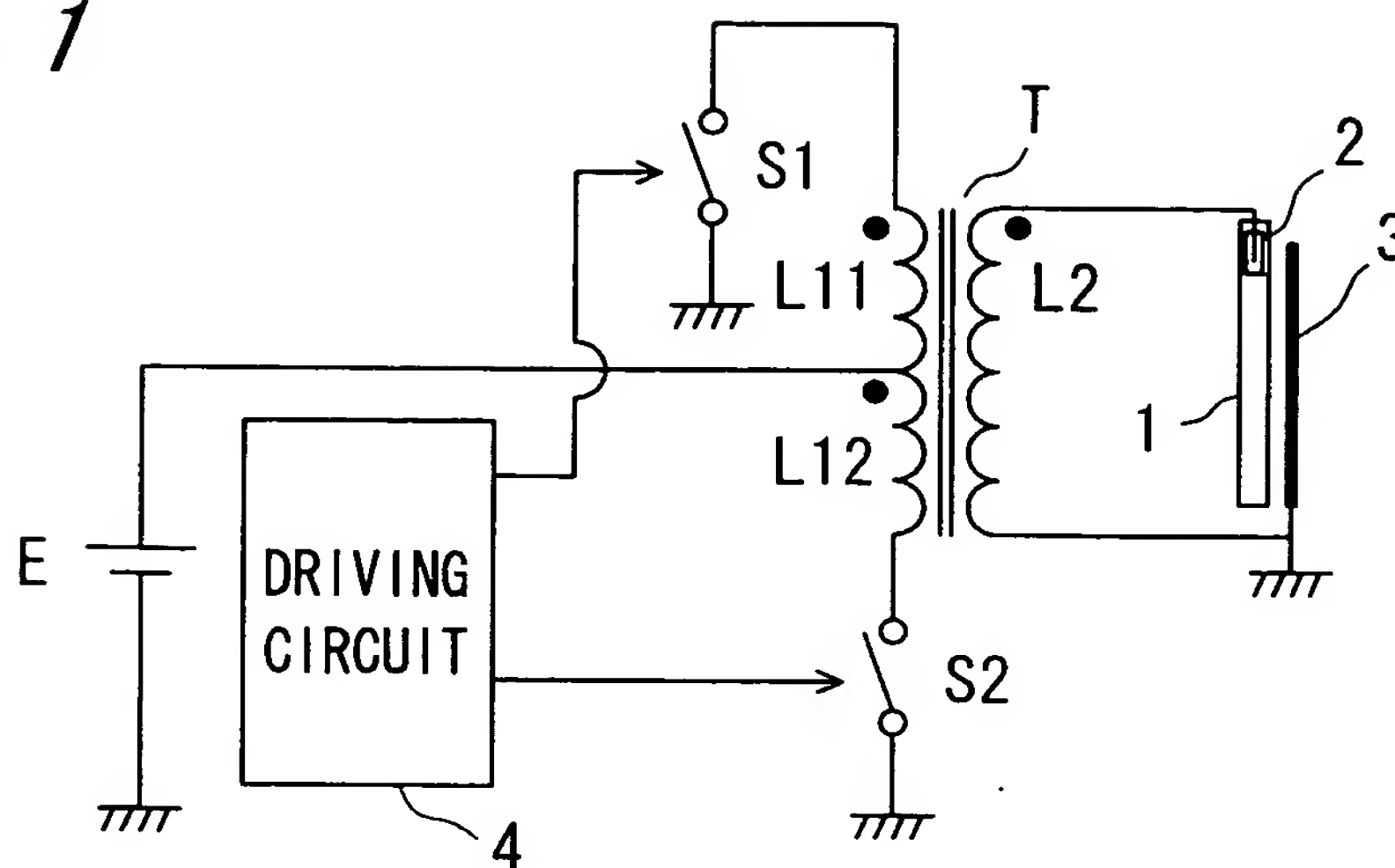
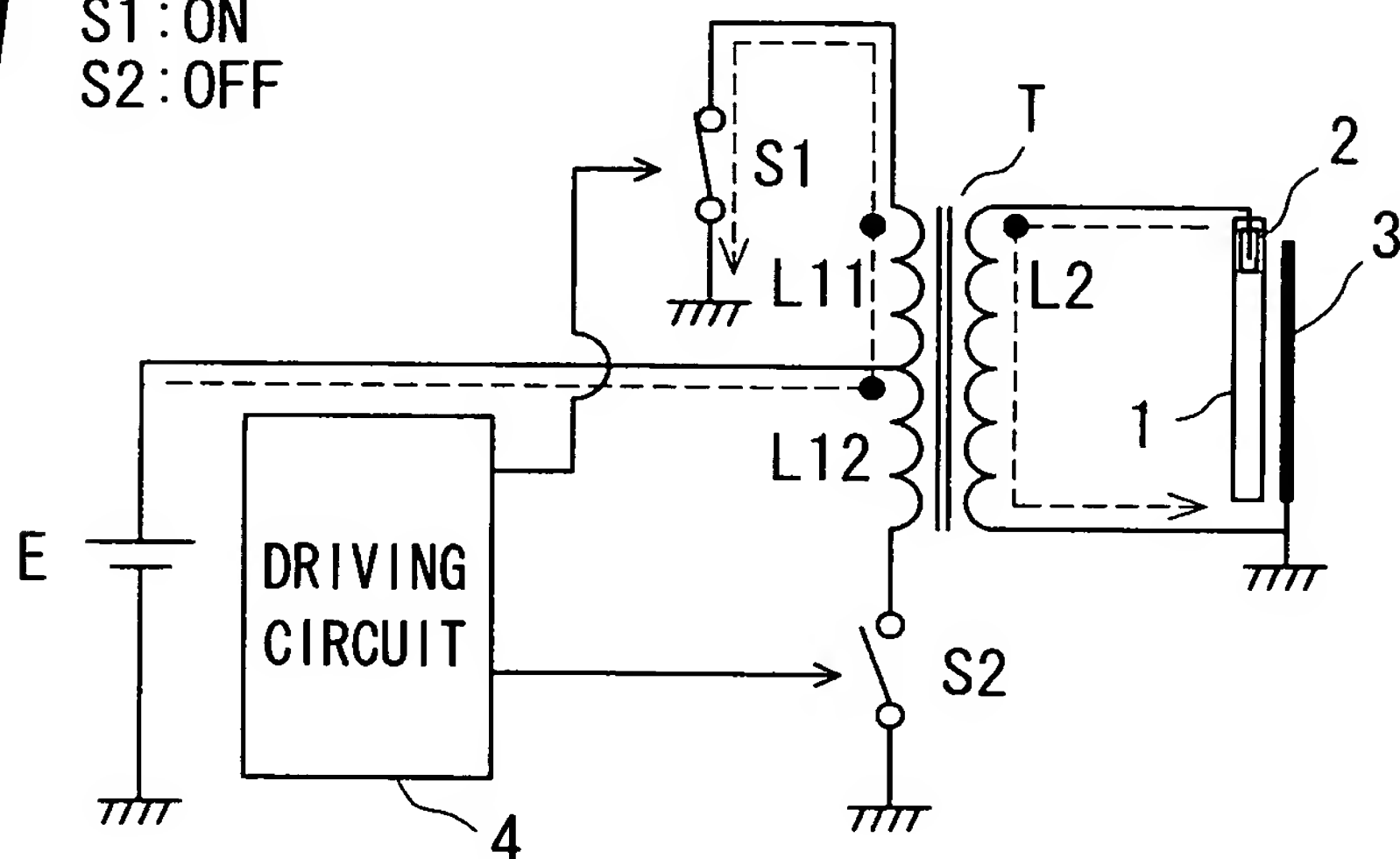
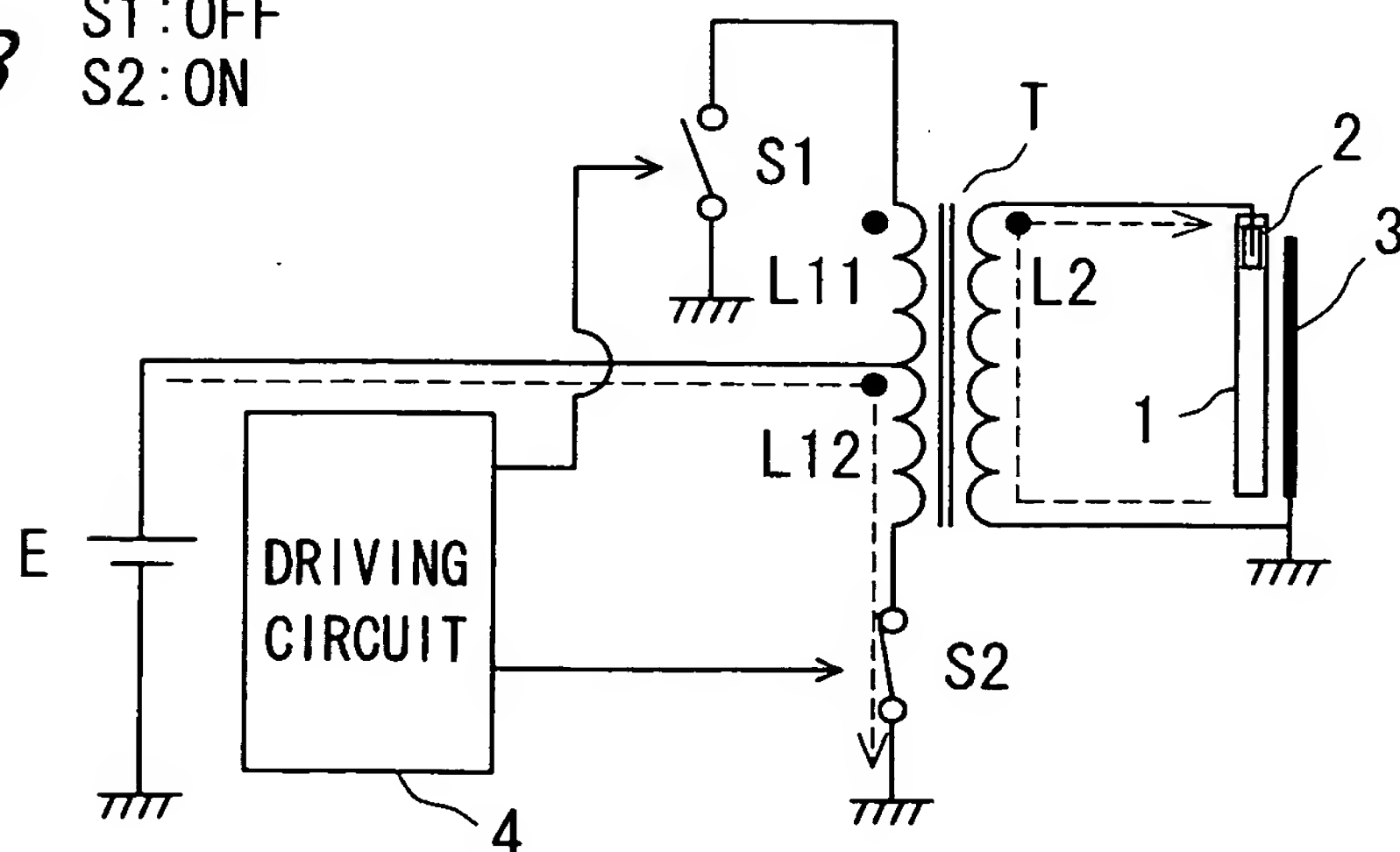
Fig. 1*Fig. 2A* S1: ON
S2: OFF*Fig. 2B* S1: OFF
S2: ON

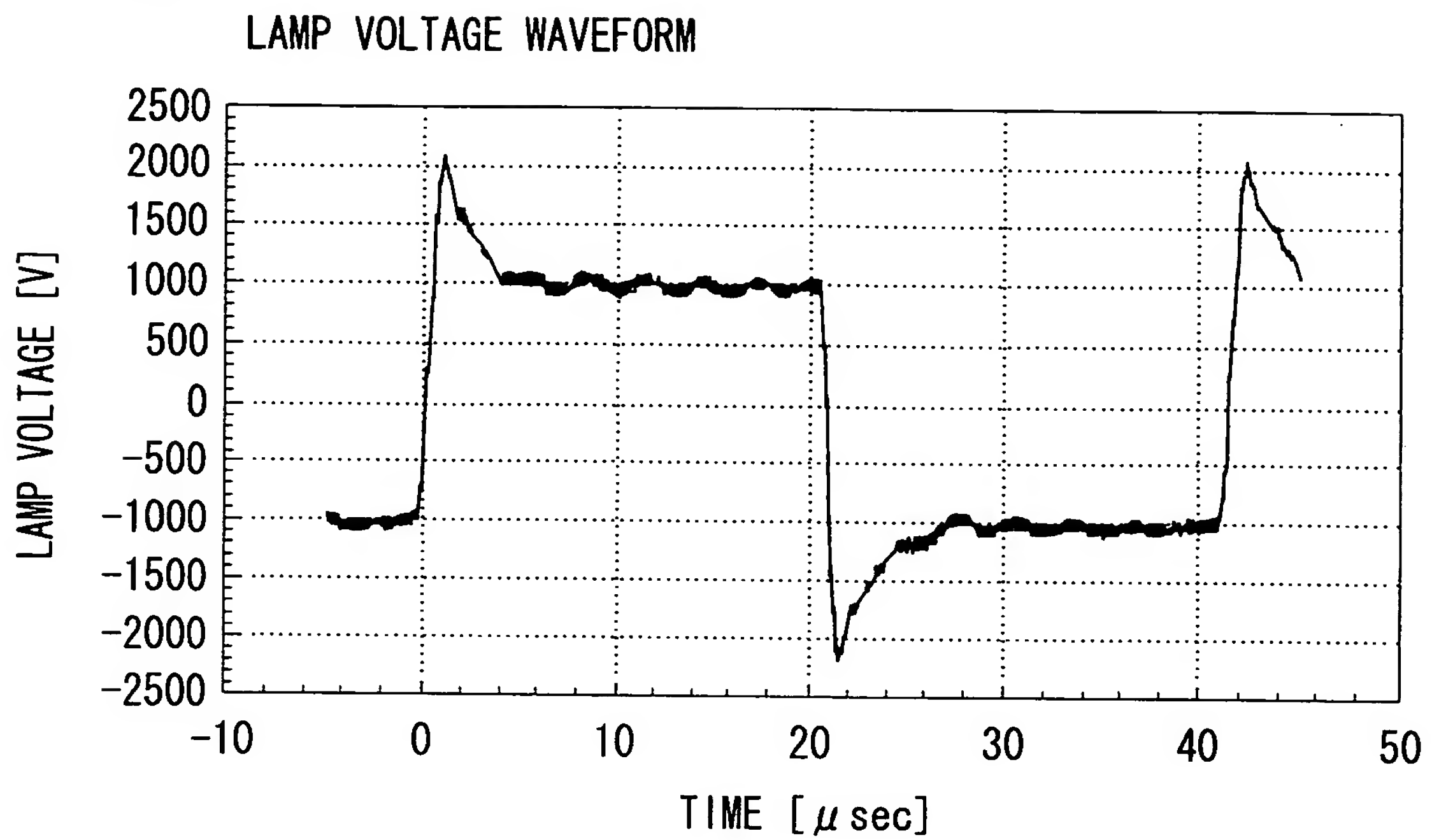
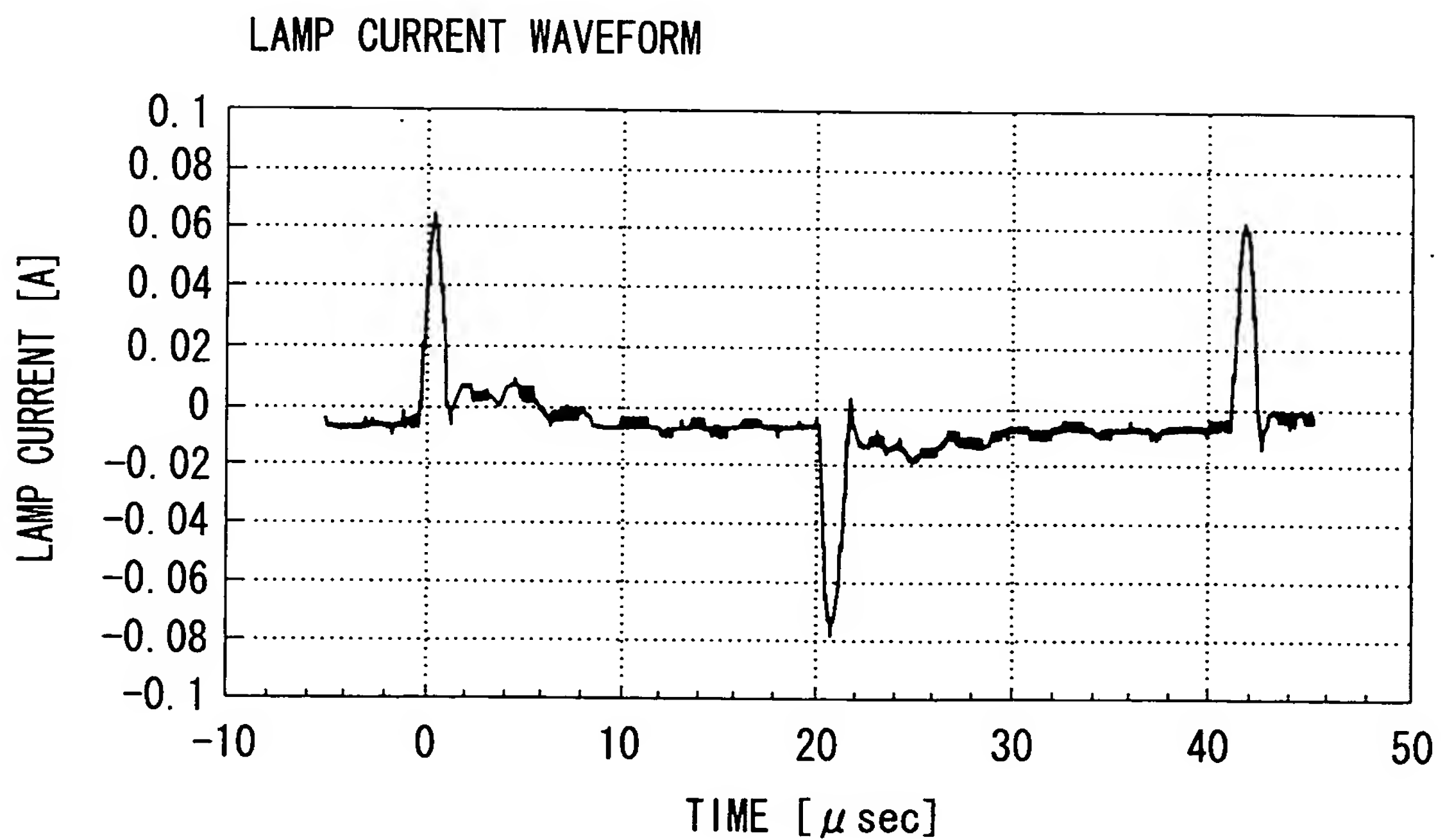
Fig. 3A*Fig. 3B*

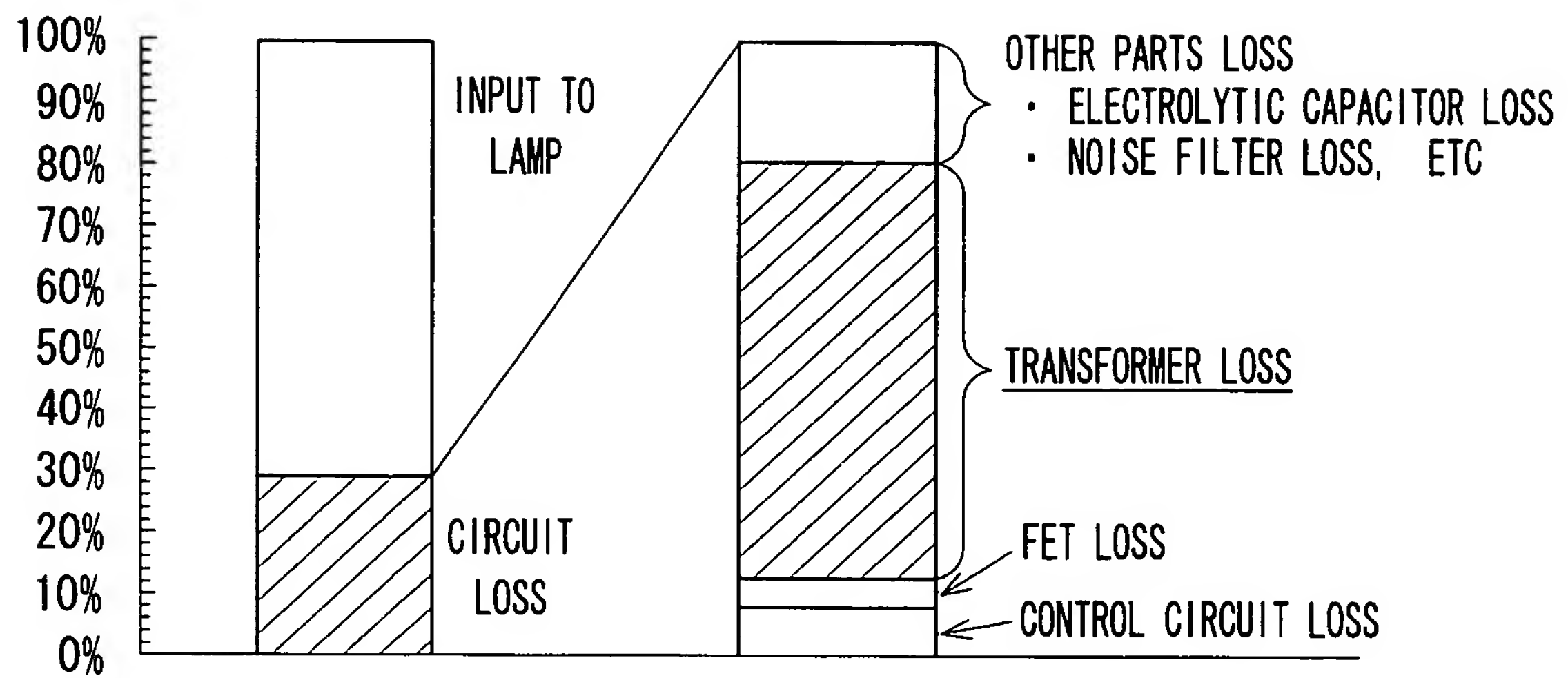
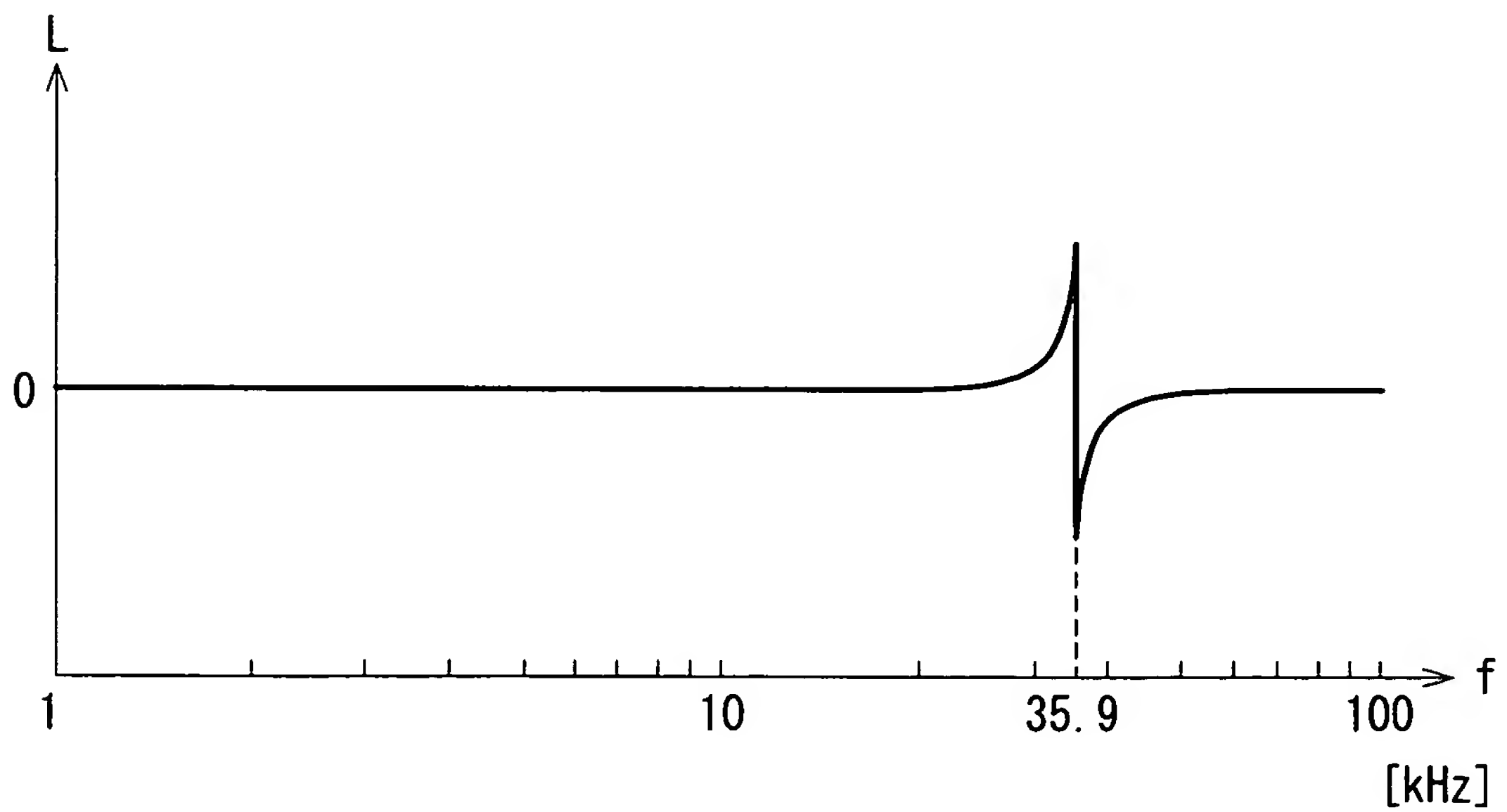
Fig. 4*Fig. 5*

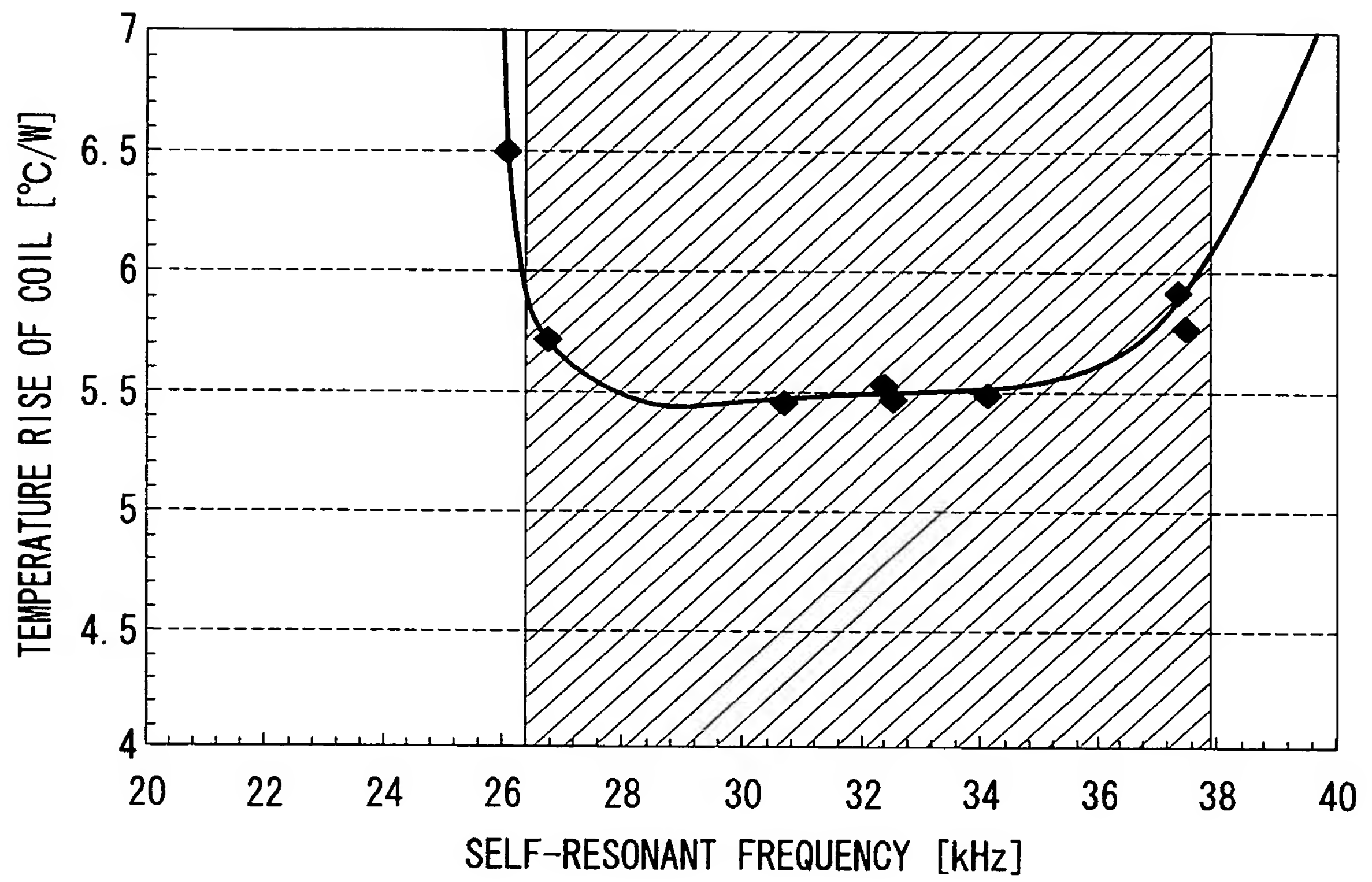
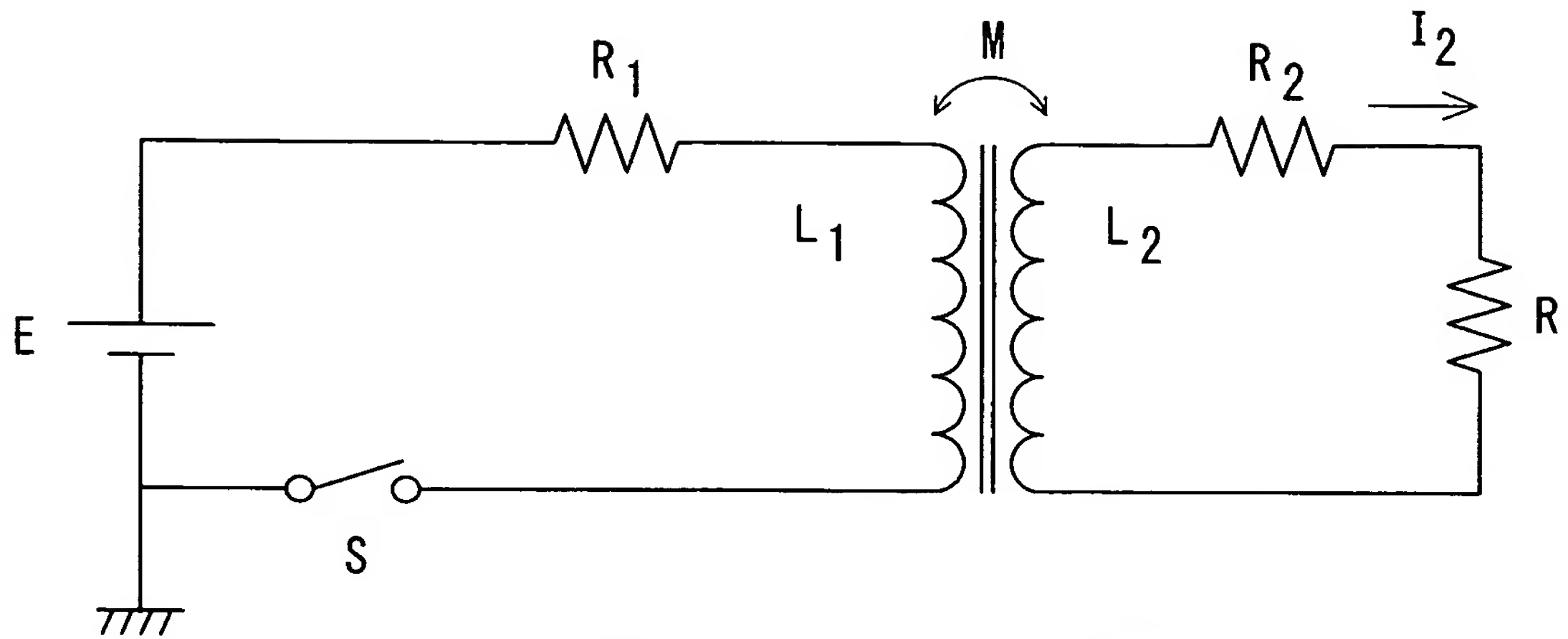
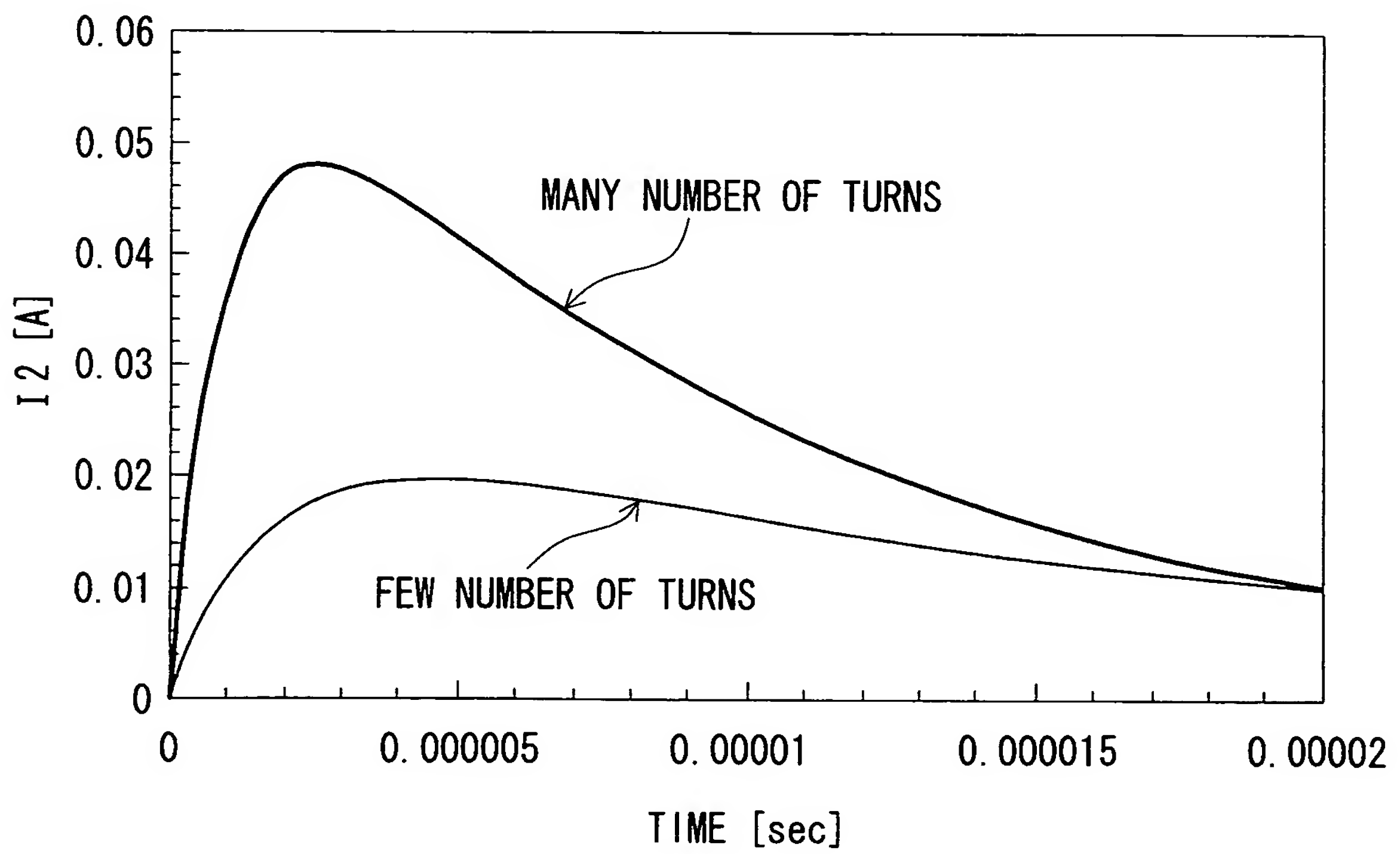
Fig. 6

Fig. 7A

ANALYSIS CIRCUIT DIAGRAM

Fig. 7B

SECONDARY CURRENT WAVEFORM

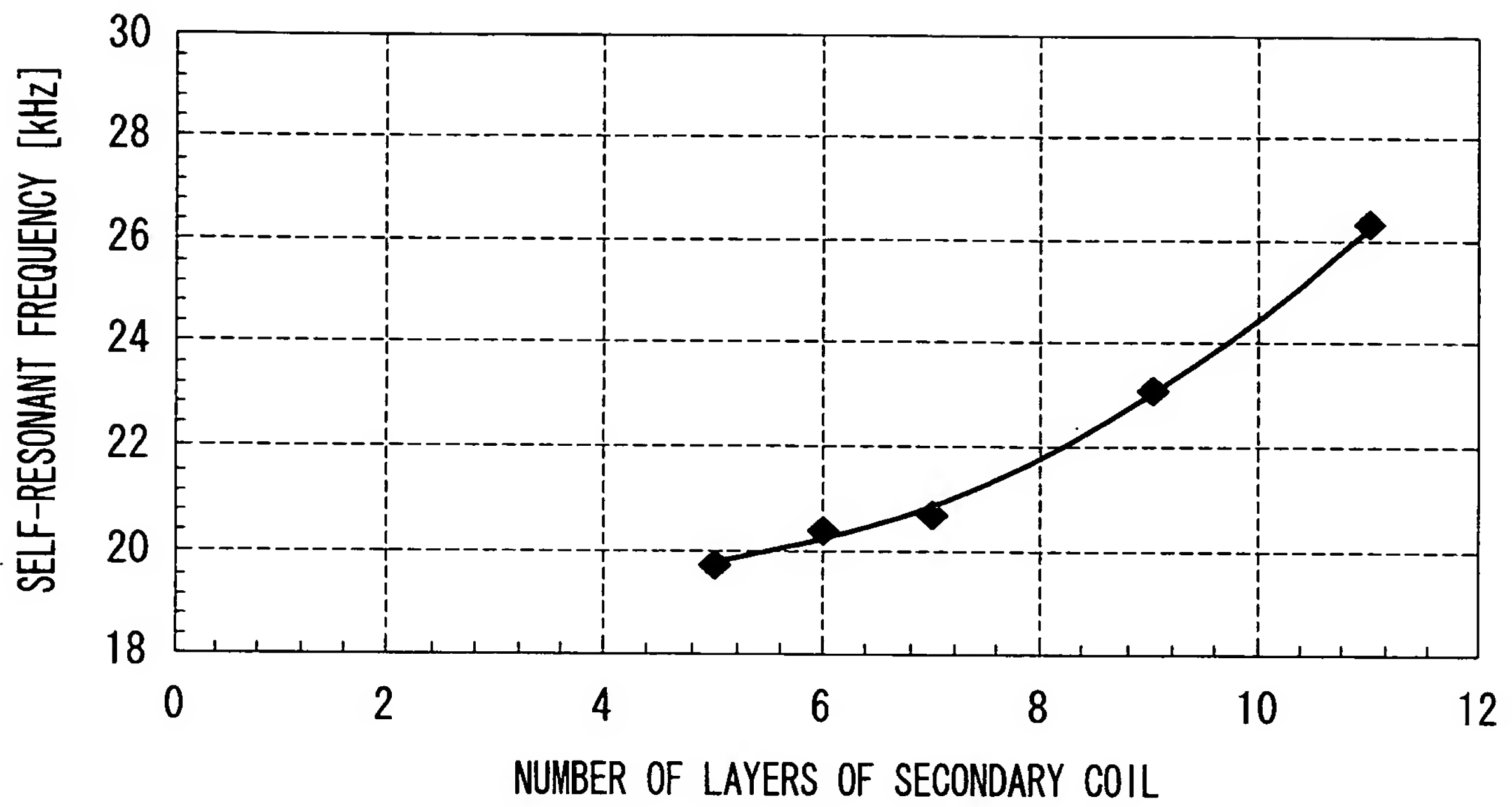
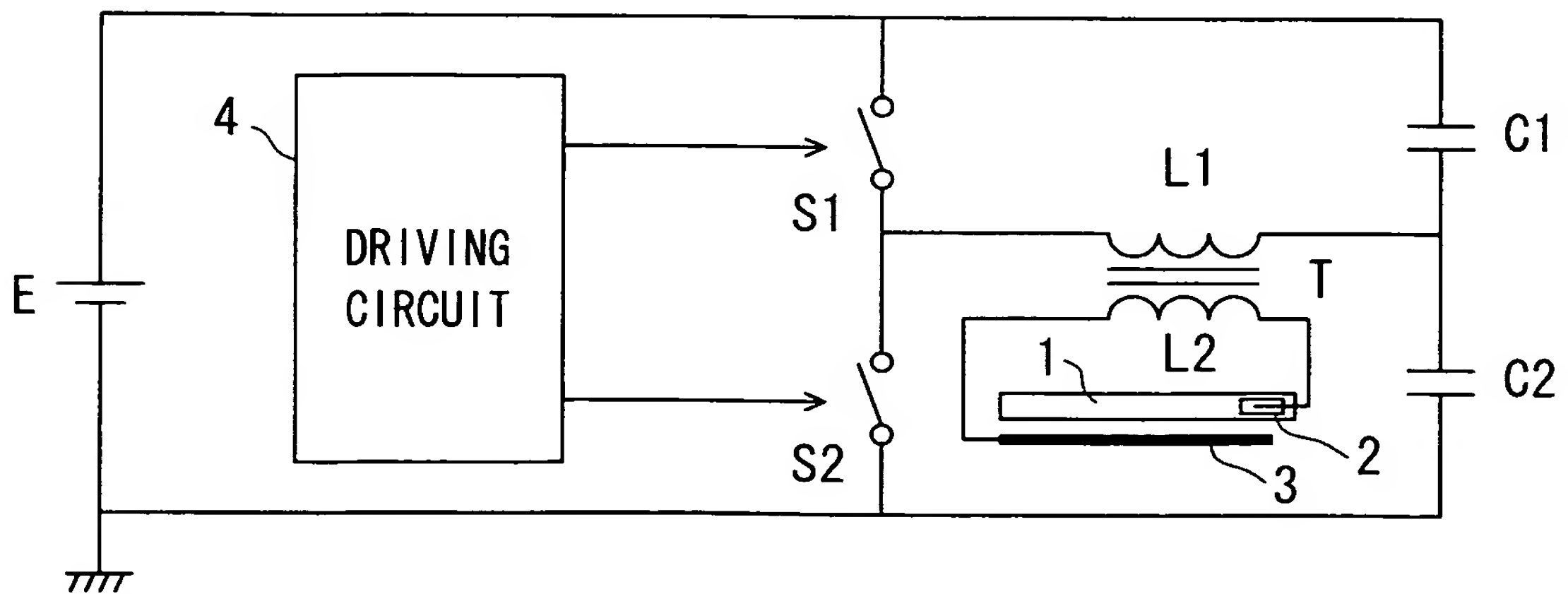
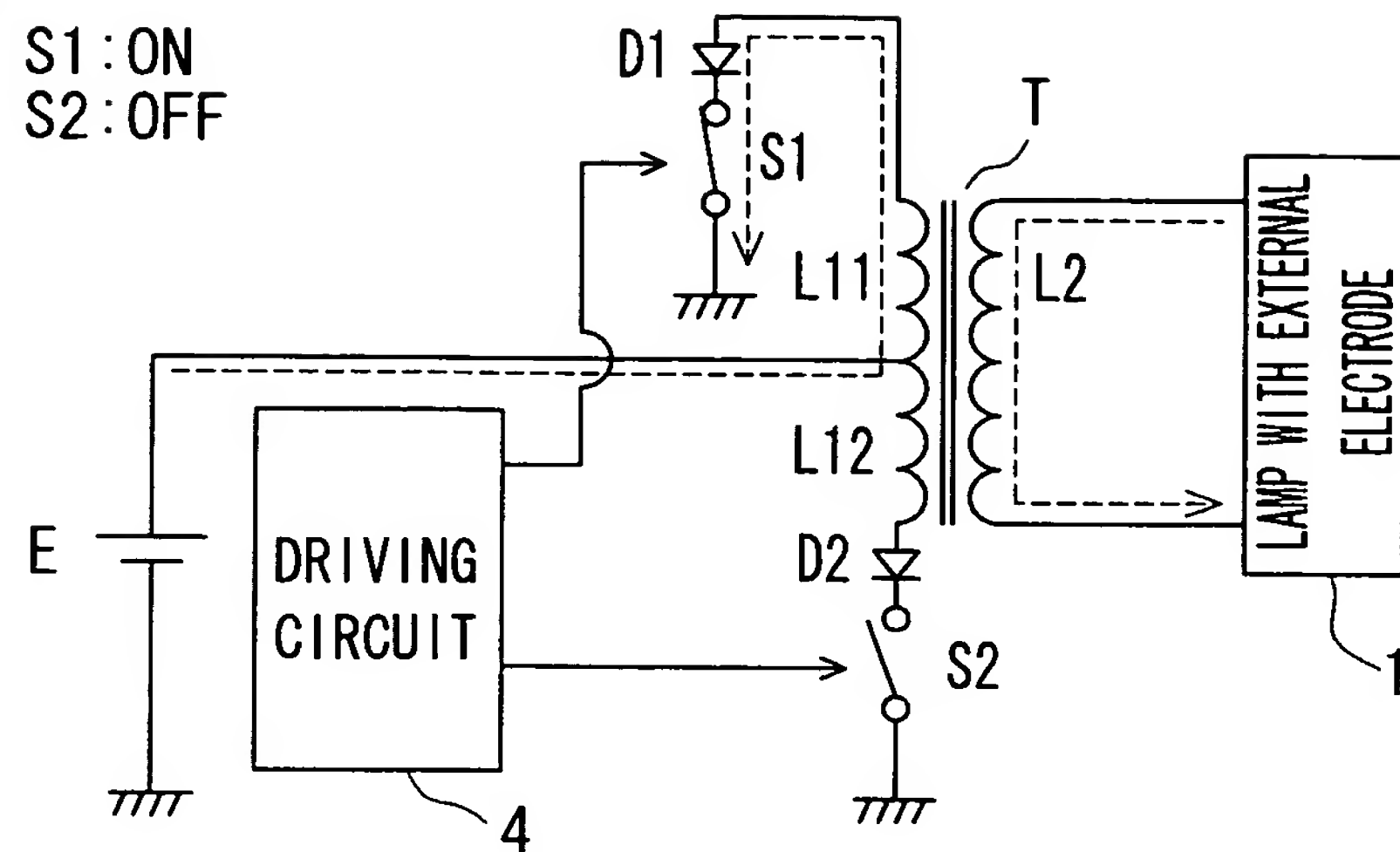
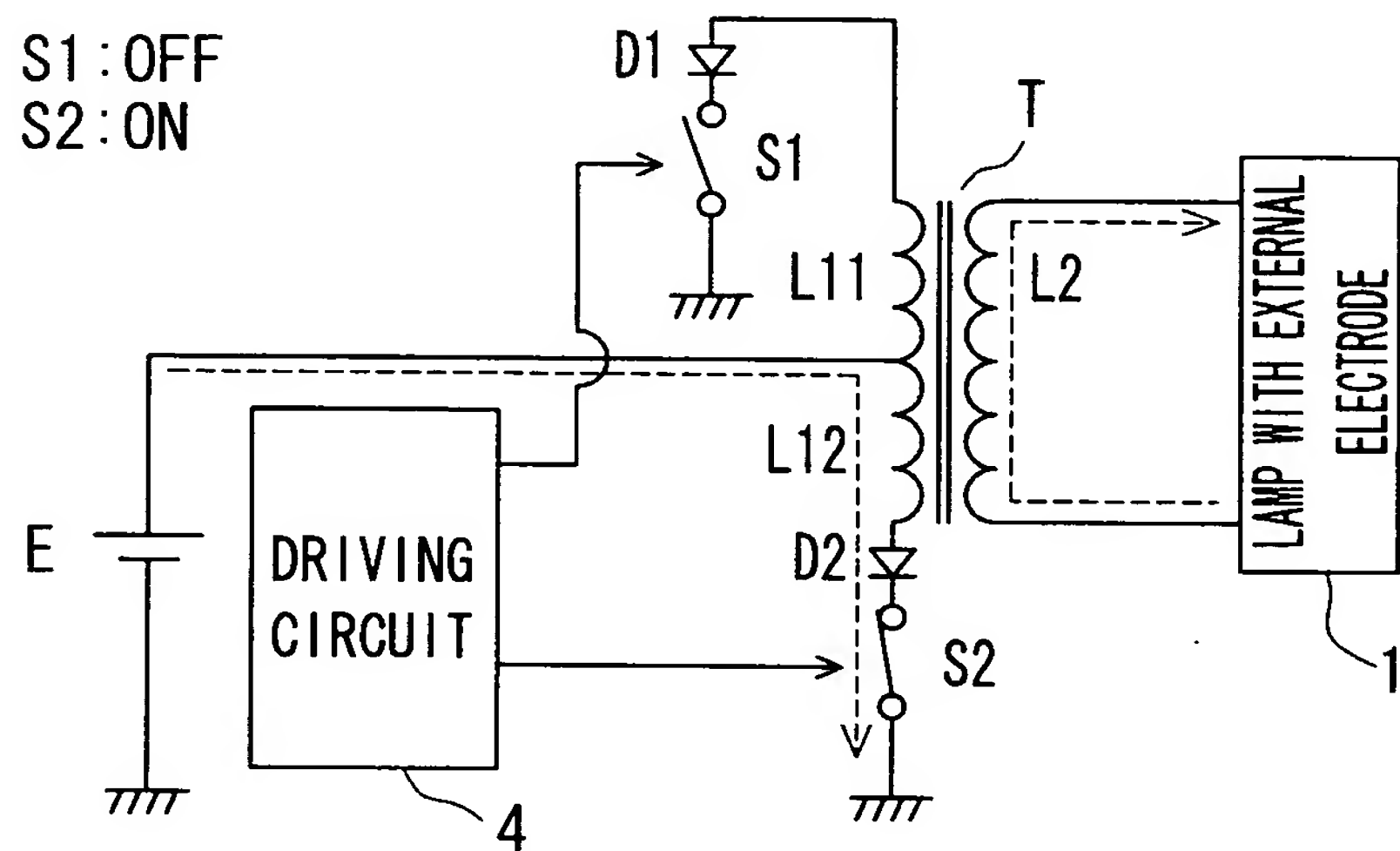
Fig. 8*Fig. 9*

Fig. 10A*Fig. 10B**Fig. 11*